



DEC 03 2001

Docket No.: J&R-0694

2133

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DEC 06 2001  
Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2100  
Applicant : Thomas Zettler  
Applic. No. : 09/922,479  
Filed : August 3, 2001  
Title : Method and Device for Testing an Integrated Circuit, Integrated Circuit to be Tested, and Wafer with a Large Number of Integrated Circuits to be Tested  
Art Unit : 2133

## **ASSOCIATE POWER OF ATTORNEY**

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

Please recognize MARK P. WEICHSELBAUM (Reg. No. 43,248) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

Lerner and Greenberg, P.A.  
P.O. Box 2480  
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Respectfully submitted,

Wen Shu

Date: November 26, 2001

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/bmb